

Reply to Office Action of 06/20/2005  
Amendment Dated: September 12, 2005

Appl. No.: 10/708,800  
Attorney Docket No.: H0005591

### Listing of Claims

1       Claim 1 (Original): A high resolution potentiometer comprising:  
2           a plurality of digital potentiometers connected in parallel.

1       Claim 2 (Original): A high resolution potentiometer comprising:  
2           a first digital potentiometer and a second digital potentiometer connected in parallel, wherein  
3           said first digital potentiometer is operable to be set to offer a first resistance and said second  
4           digital potentiometer is operable to be set to offer a second resistance, wherein said first  
5           resistance is not equal to said second resistance.

1       Claim 3 (Currently Amended): The high resolution potentiometer of claim 2, further  
2           comprising a controller block to cause said first digital potentiometer and said second digital  
3           potentiometer to respectively offer said first resistance and said second resistance.

1       Claim 4 (Currently Amended): The high resolution potentiometer of claim 3, wherein  
2           said controller block receives a desired resistance value and sets said first digital  
3           potentiometer to provide said first resistance and said second digital potentiometer to  
4           provide said second resistance such that the effective resistance provided by said high  
5           resolution potentiometer at least substantially equals said desired resistance.

1       Claim 5 (Currently Amended): The high resolution potentiometer of claim 3, wherein  
2           said controller block receives values corresponding to said first resistance and said second  
3           resistance, and sets said first digital potentiometer to provide said first resistance and said  
4           second digital potentiometer to provide said second resistance such that the effective  
5           resistance provided by said high resolution potentiometer at least substantially equals a  
6           desired resistance.

1       Claim 6. (Original): The high resolution potentiometer of claim 3, further comprising  
2           a resistor connected in series with said first digital potentiometer and said second digital  
3           potentiometer connected in parallel.

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1       Claim 7 (Currently Amended): ~~The high resolution potentiometer of claim 3, A high~~  
2       resolution potentiometer comprising:

3       a first digital potentiometer and a second digital potentiometer connected in parallel,  
4       wherein said first digital potentiometer is operable to be set to offer a first resistance and said  
5       second digital potentiometer is operable to be set to offer a second resistance, wherein said  
6       first resistance is not equal to said second resistance; and

7       a controller block to cause said first digital potentiometer and said second digital  
8       potentiometer to respectively offer said first resistance and said second resistance.

9           wherein said first potentiometer, said second potentiometer and said controller block  
10          are implemented in a single integrated circuit.

1       Claim 8. (Original): A system comprising:

2       a first digital potentiometer and a second digital potentiometer connected in parallel,  
3       wherein said first digital potentiometer is operable to be set to offer a first resistance and said  
4       second digital potentiometer is operable to be set to offer a second resistance, wherein said  
5       first resistance is not equal to said second resistance.

1       Claim 9 (Currently Amended): The system of claim 7, further comprising:

2       a controller block to cause said first digital potentiometer and said second digital  
3       potentiometer to respectively offer said first resistance and said second resistance; and  
4       a processor.

1       Claim 10 (Currently Amended): The system of claim 9, wherein said controller block  
2       receives a desired resistance value from said processor and sets said first digital  
3       potentiometer to provide said first resistance and said second digital potentiometer to  
4       provide said second resistance such that the effective resistance provided by said system at  
5       least substantially equals said desired resistance.

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1       Claim 11 (Currently Amended): The system of claim 9, wherein said controller block  
2 receives values corresponding to said first resistance and said second resistance from said  
3 processor, and sets said first digital potentiometer to provide said first resistance and said  
4 second digital potentiometer to provide said second resistance such that the effective  
5 resistance provided by said first digital potentiometer and said second digitalpotentiometer  
6 are connected in parallel at least substantially equals a desired resistance.

1       Claim 12. (Original): The system of claim 9, further comprising a resistor connected  
2 in series with said first digital potentiometer and said second digital potentiometer connected  
3 in parallel.

1       Claim 13 (Currently Amended): The system of claim 9, A system comprising:  
2       a first digital potentiometer and a second digital potentiometer connected in parallel,  
3       wherein said first digital potentiometer is operable to be set to offer a first resistance and said  
4       second digital potentiometer is operable to be set to offer a second resistance, wherein said  
5       first resistance is not equal to said second resistance;  
6       a controller block to cause said first digital potentiometer and said second digital  
7       potentiometer to respectively offer said first resistance and said second resistance; and  
8       wherein said first potentiometer, said second potentiometer and said controller block  
9       are implemented in the form of a single integrated circuit.

1       Claim 14 (New) The system of claim 13, further comprising a processor, wherein said  
2 controller block receives a desired resistance value from said processor and sets said first  
3 digital potentiometer to provide said first resistance and said second digital potentiometer  
4 to provide said second resistance such that the effective resistance provided by said system  
5 at least substantially equals said desired resistance.